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Quiz-1 v3

Daffodil International University **Computer Architecture**

Department of SWE **SE222**

ID: **Section:**

1. Draw the top view architecture of a General Purpose Processor. Define the purpose of program counter. (5+2)

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2.

Types of Instruction	Instruction Count	Cycle Count
A	35000	5
B	40000	10
C	You need to find out it	4

We have total 100000 instructions.

a) Determine the effective CPI. (3)

b) If the MIPS is 5000 then find the cpu clock speed in GHz. (3)

c) Find the execution time for the program. (2)

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