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Quiz-3 (D)

Daffodil International University

Department of SWE

ID: 241-35-

Computer Architecture

SE222

Section:

(6)

1. Multiply -5 * 3 using booth's algorithm.

2. Represent $(77.125)_{10}$ in IEEE 754 single precision format. (5)

2. For direct mapping, if a physical address contains 20 bits and among them 10 bits are used for line number and 4 bits are used for tag, then calculate the main memory size and cache memory size. (4)

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