ourdiu.com | Shared by Md Shahinur Rahman ( )

Quiz Test 01

Course Code: CIS131 **Course Title: Computer Architecture and Organization**

Exam Duration: **30 mins** Marks: **15**

Answer ALL Questions:

	Questions	Marks
1.	Which two units are combined to form the CPU?	1
2.	What is a biased exponent in floating-point representation?	1
3.	What is data representation in a computer system?	1
4.	What does ISA stand for?	1
5.	Convert the decimal number 97 into BCD format.	1
6.	Write two responsibilities of the Control Unit.	2
7.	Write two examples each of primary memory and secondary memory.	2
8.	Convert the decimal value 17.85 into binary using <9,3> fixed-point notation.	2
9.	A 16-bit register is used to store a floating-point number, where the exponent is stored in excess-128 (biased-128) form. How many bits are required for the sign, exponent and mantissa fields?	2
10.	Show how the expression A = (D*B+C) - E is implemented using a Stack ISA.	2

ourdiu.com