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# Daffodil International University

Department of Software Engineering
Faculty of Science & Information Technology

*Quiz_2 (SE222_Computer Architecture) _43 I (Fall-2025)*

ID: 242-3[redacted]

| | Marks |
|---|---|
| **Question 1.** (CLO2)<br>**Illustrate** the top level components of GP processor with Diagrams. | 15 |
| **Question 2.** (CLO2)<br>**Demonstrate** the instruction cycle state diagram without and with interrupt. | 4 |
| **Question 3.** (CLO2)<br>**Find** the Hypothetical Processor Instructions from the following generated figure. | 5 |

| | | |
|---|---|---|
| 201 | 3806 | Here, |
| 202 | 6807 | 0011 → load AC from memory |
| 203 | 7808 | 0100 → store AC to memory |
| 204 | 4809 | 0110 → Subtract to AC from memory |
| ⋮ | ⋮ | |
| 806 | 0007 | |
| 807 | 0005 | |
| 808 | 0003 | |
| 809 | 0008 | |

6